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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No.09/961,114
Filing Date09/20/2001
Inventor.....Perino et al.
ApplicantRambus, Inc.
Group Art Unit2833
Examiner
Attorney's Docket No.RB1-035USC3
Title: Chip Socket Assembly and Chip File Assembly for Semiconductor Chips

SUBSTITUTE DRAWING REQUEST

To: Drawing Review Branch
Commissioner of Patents and Trademarks,
Washington, D.C. 20231

From: Daniel L. Hayes (Tel. 509-324-9256; Fax 509-323-8979)
Customer No. 29150



29150

PATENT & TRADEMARK OFFICE

Applicant submits herewith a new set of formal drawings to correct
informalities noted by the official draftsman in Notice to File Corrected
Application Papers.

Respectfully Submitted,

Date: 12/10/01

By: Daniel L. Hayes
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